

- Structure : Silicon Monolithic Integrated Circuit
- Product name : Triple Circuits Video Signal Switchers
- Type : **BA7606F, BA7606FS**
- Features :
 - 1) Contain three 2-input, 1-output switch circuits
 - 2) Power supply voltage (4.5~5.5 V)
 - 3) Low power consumption
 - 4) Good frequency characteristics
 - 5) Large dynamic range
 - 6) Pedestal clamp input
 - 7) Large input impedance
 - 8) Fast switching speed (Typ. 50ns)

○Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	Vcc	9	V
Power dissipation	Pd	500 *1(BA7606F)	mW
		650 *2(BA7606FS)	
Operating temperature	Topr	-40~+85	°C
Storage temperature	Tstg	-55~+125	°C

- *1 Deratings is done at 5.0mW/°C above Ta=25°C.
- *2 When mounted on a 70mm × 70mm × 1.6mm PCB board
Deratings is done at 6.5mW/°C above Ta=25°C.

○Operating Range (Ta=25°C)

Parameter	Symbol	Min	Typ	Max	Unit
Power supply voltage	Vcc	4.5	5.0	5.5	V

* This product is not designed for protection against radioactive rays.

Application example

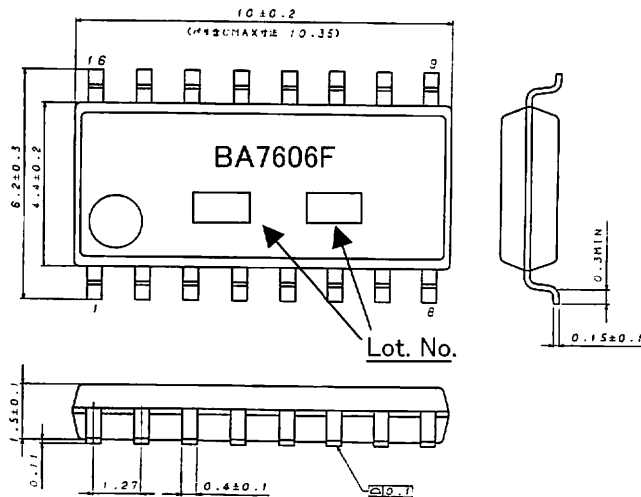
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○Electrical characteristics (Unless otherwise noted, Ta= 25°C, Vcc=5.0V)

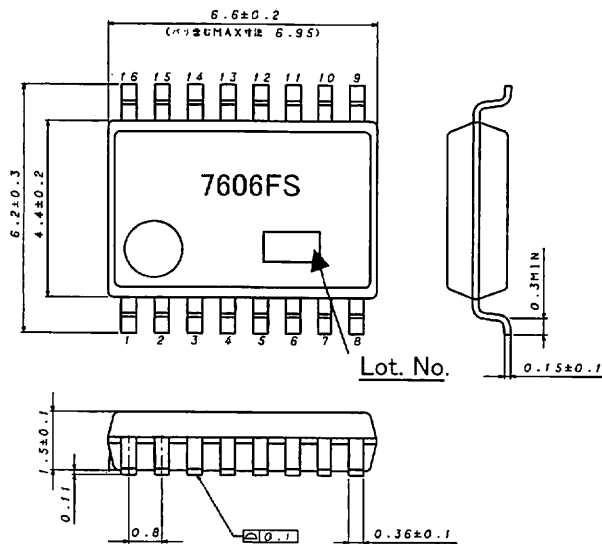
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Circuit current	I _{CC}	-	15.0	23.0	mA	
Maximum output level1	V _{OM1}	1.40	1.65	-	V _{p-p}	*1
Maximum output level2	V _{OM2}	0.80	0.95	-	V _{p-p}	*2
Voltage gain	G _v	-0.9	0	0.5	dB	f=1MHz, V _{IN} =1 V _{P-P}
Interchannel crosstalk	C _T	-	-65	-	dB	f=4.43MHz, V _{IN} =1 V _{P-P}
Frequency characteristic	G _f	-3	-1	1	dB	10MHz/1MHz, V _{IN} = 1V _{P-P}
CTL pin switching level	V _{TH}	2.0	2.5	3.0	V	
Clamp input level	V _{Cl}	0.75	-	2.2	V	

- *1 Positive-side dynamic range from the clamp level
- *2 Negative-side dynamic range from the clamp level

○Outer dimensions

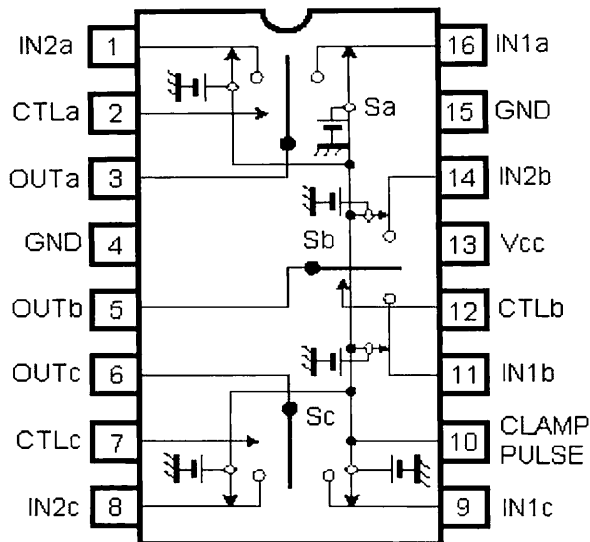


SOP16 (Unit: mm)



SSOP-A16 (Unit: mm)

○Block diagram



○Pin number and pin name

Pin No.	Pin name
1	IN2a
2	CTLa
3	OUTa
4	GND
5	OUTb
6	OUTc
7	CTLc
8	IN2c
9	IN1c
10	CLAMP PULSE
11	IN1b
12	CTLb
13	Vcc
14	IN2b
15	GND
16	IN1a

○Cautions on use

1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

2) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.

3) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

4) Shorts between pins and miss-installation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is miss-installed and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

5) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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